

WEST Search History

DATE: Monday, August 22, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L33	L30 and l21	4
<input type="checkbox"/>	L32	L30 and ((first near5 second near5 third) near5 (interface or bus))	149
<input type="checkbox"/>	L31	L30 and l3	22
<input type="checkbox"/>	L30	l29 or l28 or l27 or l26 or l25 or l24	5158
<input type="checkbox"/>	L29	712/35	419
<input type="checkbox"/>	L28	710/29	630
<input type="checkbox"/>	L27	710/72	799
<input type="checkbox"/>	L26	710/305	1855
<input type="checkbox"/>	L25	713/320	1539
<input type="checkbox"/>	L24	(713/310)[CCLS]	367
<input type="checkbox"/>	L23	(multiple processor or multi\$1processor or plural\$ processor) near10 ((first near5 second near5 third) near5 (interface or bus))	4
<input type="checkbox"/>	L22	L21.ti,ab,clm.	22
<input type="checkbox"/>	L21	((multiple processor or multi\$1processor or plural\$ processor) or (cpu near10 dsp)) near10 (shar\$ near5 memory near5 i\$lo)	129
<input type="checkbox"/>	L20	(cpu near10 dsp) near10 (soc or system on chip or package)	21
<input type="checkbox"/>	L19	(5418976 5432804 5440504 5454014 5491771 5491828 5504784 5550870)![pn]	8
<input type="checkbox"/>	L18	(multiple processor or multi\$1processor or plural\$ processor) same ((dedicated or separate) near5 program near3 memory)	16
<input type="checkbox"/>	L17	l3 same (dedicated or separate) near5 memory	2
<input type="checkbox"/>	L16	l3 same program	16
<input type="checkbox"/>	L15	l3 same program memory	0
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L14	6678765[uref]	0
<input type="checkbox"/>	L13	6327648[uref]	4
<input type="checkbox"/>	L12	6327648.pn.	1
<input type="checkbox"/>	L11	L3 same (cellular)	0
<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L10	L3 same (celular)	0
<input type="checkbox"/>	L9	L3 same (mobile or portable)	5

<input type="checkbox"/>	L8	L3 same (wireless)	6
<input type="checkbox"/>	L7	L3 same (chip)	21
<input type="checkbox"/>	L6	L3 same (on chip)	0
<input type="checkbox"/>	L5	L3 same (package or soc)	4
<input type="checkbox"/>	L4	L3.ti,ab.	24
<input type="checkbox"/>	L3	(multiple processor or multi\$1processor or plural\$ processor) near10 (multiple interface or multi\$1interface or multiple bus or multi\$1bus)	431
<input type="checkbox"/>	L2	l1 same dsp	4
<input type="checkbox"/>	L1	(multiple processor or multi\$1processor or plural\$ processor) near10 (soc or system on chip)	45

END OF SEARCH HISTORY